

SYSTEM AND METHOD FOR REDUCING CHARGE
DAMAGE IN SILICON ON INSULATOR TECHNOLOGY

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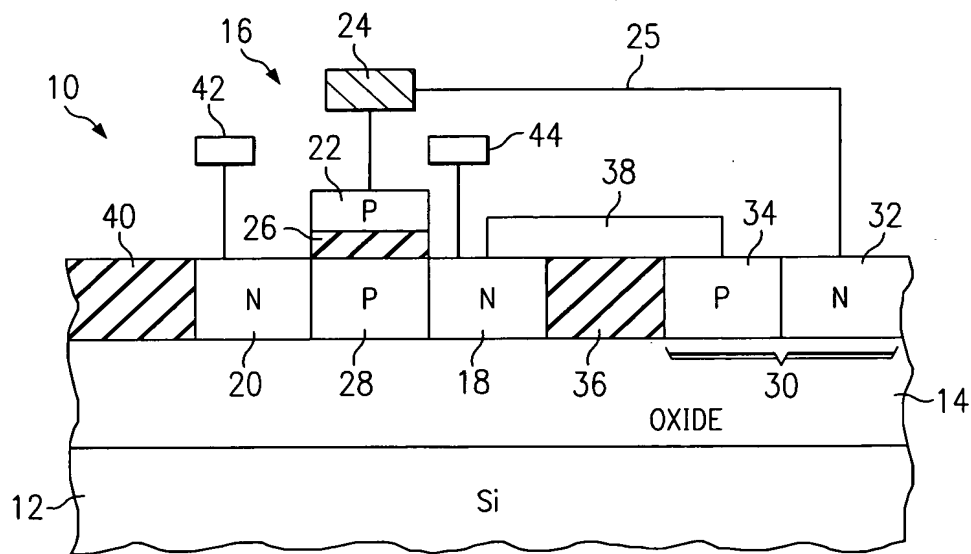


FIG. 1A

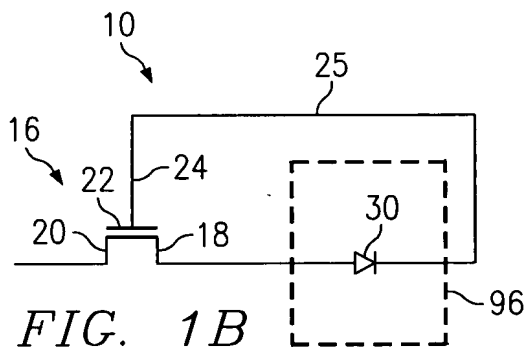


FIG. 1B

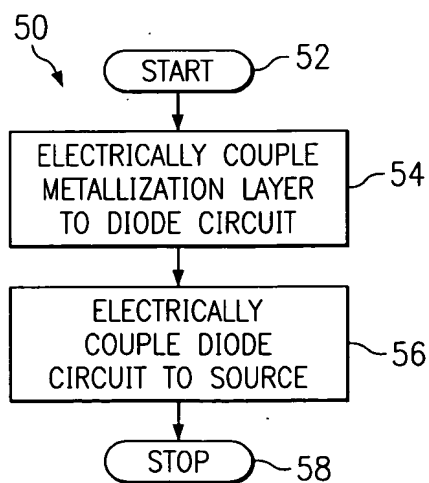


FIG. 1C

10035606.422801

FIG. 2A

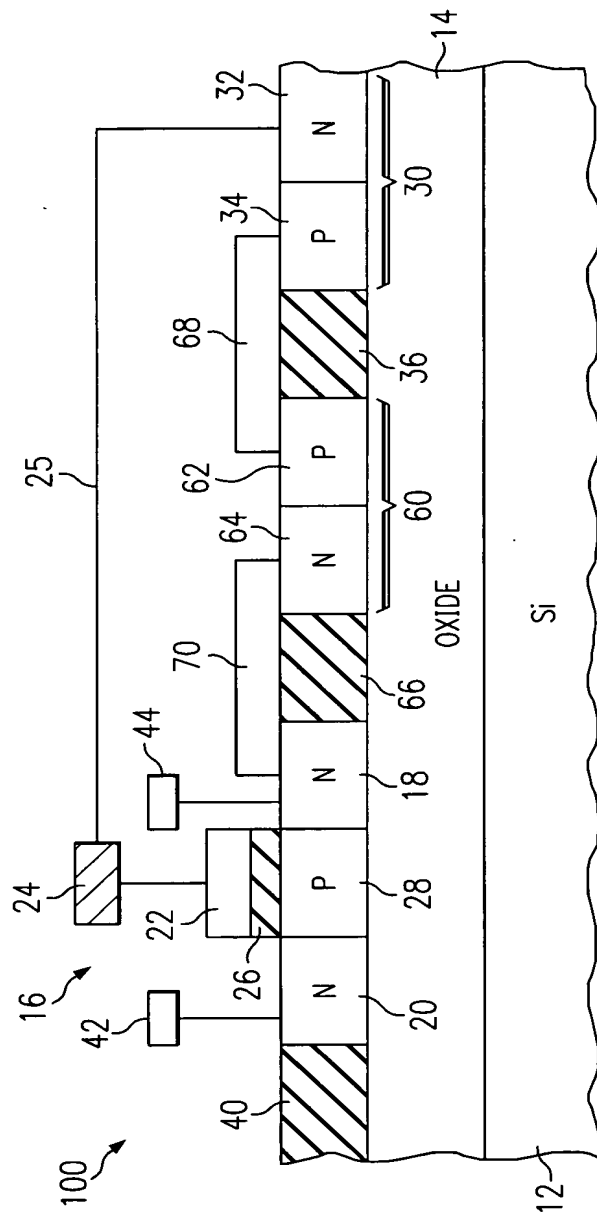


FIG. 2A

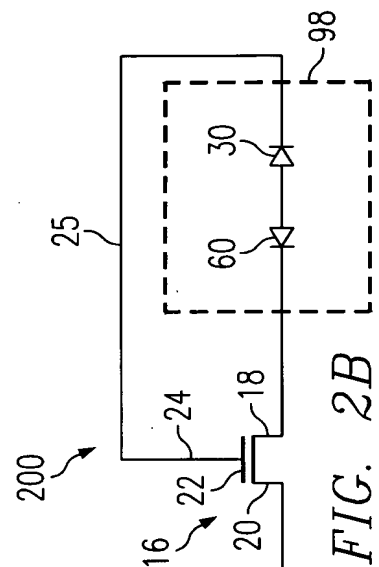


FIG. 2B

100-443886-100

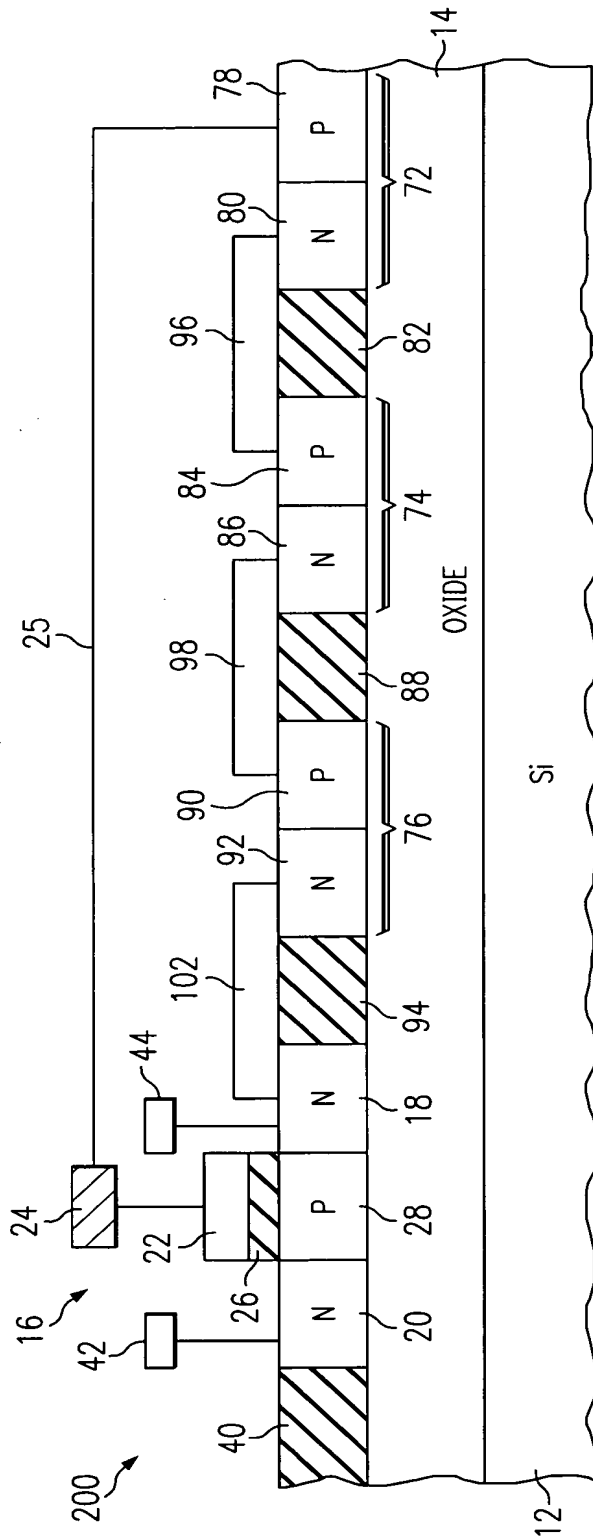


FIG. 3A

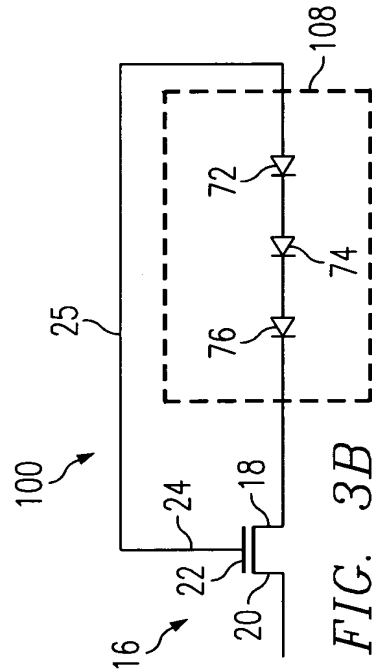


FIG. 3B